

1503.68591

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Makoto Nakanishi
Serial No.: 10/692,533
Conf. No.: 9889
Filed: 10/24/2003
For: PARALLEL PROCESSING METHOD FOR
INVERSE MATRIX FOR SHARED
MEMORY TYPE SCALAR PARALLEL
COMPUTER
Art Unit: 2127
Examiner: Not assigned

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Amendment, Commissioner for Patents, P.O.
Box 1450, Alexandria, VA 22313-1450, on this
date.*

24/9/05
DateJCSB
Registration No. 29,367
Attorney for Applicant(s)INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This IDS is submitted under 37 C.F.R. §1.97(b) within either of the following time periods, whichever occurs last:

- (a) within three months of either the filing date of the national application or the date of entry into the national stage; or
- (b) before the mailing date of first office action on the merits (i.e., not including actions such as restriction requirements).

Applicant(s) submit herewith Form PTO-1449 (Information Disclosure Citation) together with copies of foreign patents, publications or other information of which applicant(s) are aware, which applicant(s) believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. §1.56. Applicant(s) respectfully submit that the citation of any reference on Form PTO-1449 does not constitute an admission that the reference qualifies as prior art.

It is requested that the information disclosed on the enclosed Form PTO-1449 be made of record in this application.

The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or to credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Form PTO-1449 U.S. Department of Commerce
(Rev. 8-88) Patent and Trademark Office

Attorney Docket No.: 1503.68591

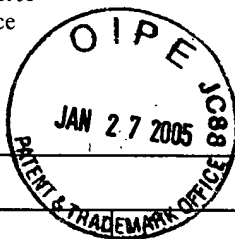
Serial No.: 10/692, 533

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Applicant: Makoto Nakanishi

Filing Date: 10/24/03

Group: 2127



U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	5,596,518	Jan. 21, 1997	Toyokura et al.			
	5,301,342	Apr. 5, 1994	Scott			
	5,561,784	Oct. 1, 1996	Chen et al.			
	5,428,803	Jun. 27, 1995	Chen et al.			
	5,333,117	Jul. 26, 1994	Hat et al.			
	5,251,097	Oct. 5, 1993	Simmons et al.			
	5,226,171	Jul. 6, 1993	Hall et al.			
	5,490,278	Feb. 6, 1996	Mochizuki			
	5,251,097	Oct. 5, 1993	Simmons et al.			

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Huang et al; "Efficient Storage Scheme and Algorithms for W-Matrix Vector Multiplication on Vector Computers"; IEEE Trans. on Power Systems, Vol. 9, No. 2; pp. 1083-1091; May 1994.
	Mrityunjoy Chakraborty; "An Efficient Algorithm for Solving General Periodic Toeplitz Systems"; IEEE Trans. on Signal Processing, Vo. 46, No. 3; pp. 784-787; March 1998.

Examiner

Date Considered

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.